

**Amendments to the Abstract:**

Please replace the previous Abstract with the following redlined Abstract:

A coprocessor circuit for processing image data in digital form, having a motion vector controller block for generating, starting from ~~said-the~~ image data, motion vector values. ~~Such vector values that~~ include predictor data and macroblock data relating to a current macroblock of ~~said-the~~ image data to be estimated, ~~the prediction data and macroblock data and~~ being adapted to be stored at respective memory addresses. ~~An~~ Also included is an address generator block ~~is provided for extracting said-respective addresses from said-the~~ motion vector values. ~~A~~ a predictor fetch block for retrieving ~~said~~-predictor data based on respective addresses extracted by ~~said-the~~ address generator block, a current macroblock fetch and distengine block for retrieving ~~said~~-macroblock data based on respective addresses extracted by ~~said-the~~ address generator block and for processing ~~said-the~~ macroblock data according to a given function, ~~all provided, as well as and~~ a decision block for collecting ~~said-the~~ retrieved data as partial results and selecting the best result therefrom.